

PLASMA DISPLAY APPARATUS

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ABSTRACT OF DISCLOSURE

An ALIS system PDP apparatus in which a reduction in
the variation in luminance due to ringing has been
10 reduced is disclosed. In an ALIS system PDP apparatus, X
(first) electrodes and Y (second) electrodes are arranged
in such a way that the X electrodes and the Y electrodes,
whose path length, which is the length of each of the
15 signal paths of a sustain discharge pulse from the X and
Y electrode to sustain drive circuits, is longer or
shorter than that of the peripheral X and Y electrodes,
do not exist densely. Specifically, the order of
arrangement of the odd and even X sustain drive circuits
is reversed to that of odd and even Y sustain drive
20 circuits.